

Notice of References Cited	Application/Control No. 10/003,184	Applicant(s)/Patent Under Reexamination REBLEWSKI, FREDERIC	
	Examiner Kandasamy Thangavelu	Art Unit 2123	Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,777,489	07-1998	Barbier et al.	326/40
	B	US-6,265,894	07-2001	Reblewski et al.	326/39
	C	US-2004/0078187	04-2004	Reblewski, Frederic	703/028
	D	US-6,052,524	04-2000	Pauna, Mark R.	703/22
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Ni et al., "A 256X256 pixel smart CMOS image sensor for line-based stereo vision applications", IEEE 2000.
	V	Miyake et al., "A gesture controlled human interface using an artificial retina chip", IEEE 1996
	W	Osterman et al., "Superconducting infrared detector arrays with infrared processing circuitry", IEEE 1991.
	X	Agusleo et al., "Employing logic-enhanced memory for high performance ATM network interfaces", IEEE 1996

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Vries et al., "Built-in self-test methodology for A/D converters", IEEE 1997.
	V	Azais et al., "A low-cost adaptive ramp generator for analog BIST applications", IEEE, May 2001.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.